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Robert Faber 11 OCTOBER 2005
(Signature & date)

10/552971
JCO5 Rec'd PCT/PTO 11 OCT 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : 11 October 2005
Tonti, et al. : Group Art Unit: Unassigned
Serial No: Unassigned : Examiner: Unassigned
Filed: Herewith : International Business Machines Corporation
2070 Route 52
Hopewell Junction, NY 12533

TITLE: Programmable Semiconductor Device

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit copies of the US and non-US patents and publications as listed on Form PTO-1449, attached hereto.

In citing these documents, no representation is made nor intended as to the pertinency or non-pertinency of the art, that better art than that listed is not available, or that other art is not applicable.

No fee is believed to be due for this submission. If any fees are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,
Tonti, et al.

By Joseph P. Abate 11-Oct 2005
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BUR920020076US2

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	Docket Number (Optional) BUR920020076US2	Application Number Unassigned
	Applicant(s) Tonti, et al.	
	10/552971	
	Filing Date Herewith	Group Art Unit Unassigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,334,880	8-2-94	Abadeer, et al.			
		4,229,502	10-21-80	Wu, et al.			
		4,309,224	1-5-82	Shibata			
		6,433,404	8-13-02	Iyer, et al.			
		6,084,796	7-4-00	Kozicki, et al.			

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

		Kothandaraman, et al., "Electrically Programmable Fuse (eFUSE) Using Electromigration in Silicides". IEEE Electron Device Letters, Vol. 23, No. 9, September 2002.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.